

MAM SCHOOL OF ENGINEERING SIRUGANUR, TRICHY DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

No that there

1000	Name of the original month	Programme.
Section 1	Verification of Boolean Theorems using Deptal Logic Gates	- 4
-	Design and Engigementation of Communicational Carcuits using Bank Cafer for Arbitrary Functions, Code Conserters, Etc.	10
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7.	Design and Implementation of Shift Registers.	3.3
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*	Simulation of Combinational Circuits using Hardware Description Language (VHDL:/ Veriling HDL Software Registers).	3.5
10.	Simulation of Sequential Circuits using HDL (VHDL) Venilog HDL Software Required):	3.7

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EC1208-DIGITAL LABORATORY II YEAR/III SEM- CSE

BOOK NOOM II

STUDY OF LOGIC GATES.

AIME

To study about logic gates and verify their truth tables.

CS2207

APPARATUS REQUIRED:

St. 760.	COMPONENT.	SPECIFICATION	CHAC
	AND GATE	BC 7408	
2.	OR GATE	BC 7432	
3.	NOTGATE	307 7404	
- 4	NAND GATE 2 FP	RC 7400	
- 5.	NOR GATE	EC 7402	1
- 6.	X-OR GATE	RC 7486	
- 7-	SOOSED GATE FOR	9C 7410	7 _
- B.	IC TRAINER KIT	74	100
9.	PATCH CORD		14 1

THURSDAY

Circuit that takes the Ingical decision and the process are called a legic gates. Each gate has one or more input and only one expect OR. AND and NOT are busic gates. NAND, NOR and X-OR are known as universal gates. Basic gates form these gates.

AND GATES

The AND gate performs a logical tradition tremmonly known

OR GATE:

The OR gate performs a logical addition commonly known as OR function. The output is high when any one of the inputs is high. The output is low level when both the magnets are low.

NOT GATE:

The NOT gate is calculate invertor. The output is high when the input is just. The output is low-when the input is high.

NAMES OF THE

The MAND gale is a contraction of AND-NOT. The output is high when both inputs are low and any one of the input is low. The output is low and when both inputs are high.

MACHINE SEATEN

the NOR gate is a contraction of OR-NOT. The output is high seen both inputs are low. The output is low when one or both inputs are high

N-OR GATE:

The output is high when any one of the inputs is high. The output is low when both the inputs are low and both the inputs are high.

PROCEDURE

- 63. Connections are given as per circuit diagram.
- (iii). Logical inputs are given as per circuit diagram.
- (iii). Observe the output and verify the truth table.

Cs2207 Lab Manual

Robert W. Widing

Cs2207 Lab Manual:

CSCI A110, Introduction to Computers and Computing, 2007 UIC Chem 100 Robert W. Widing, 2012 General Chemistry I SCC201 Lab Manual Dionne Miller, 2015-03-02 **Integrated Science Course 1**,2005* **Course Mastery** Supplement Lee Dahringer, Dennis Guseman, Stanley Garfunkel, 1979-02-01 Live Lab Manual for State Fair Community College Chemistry Courses Shayna Burchett, 2017-05-08 Integrated Science Lab Manual Shonique R. Ebanks, Shonique R Ebanks Msc, 2016-07-20 This Science Lab Manual consists of over 30 lab based experiments for students aged 12 17 years The manual also guides students on how to write up experiments and outlines for teachers the skills being tested for and suggestions for discussions Pkg Acp Cer Chem 1101 Lab Manual Brooks/Cole,2011-03-07 **Chem 1280 Lab Manual** Norman E. Griswold, 2017 Exam 70-215 Project Lab Manual Laudon, 2003-07

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