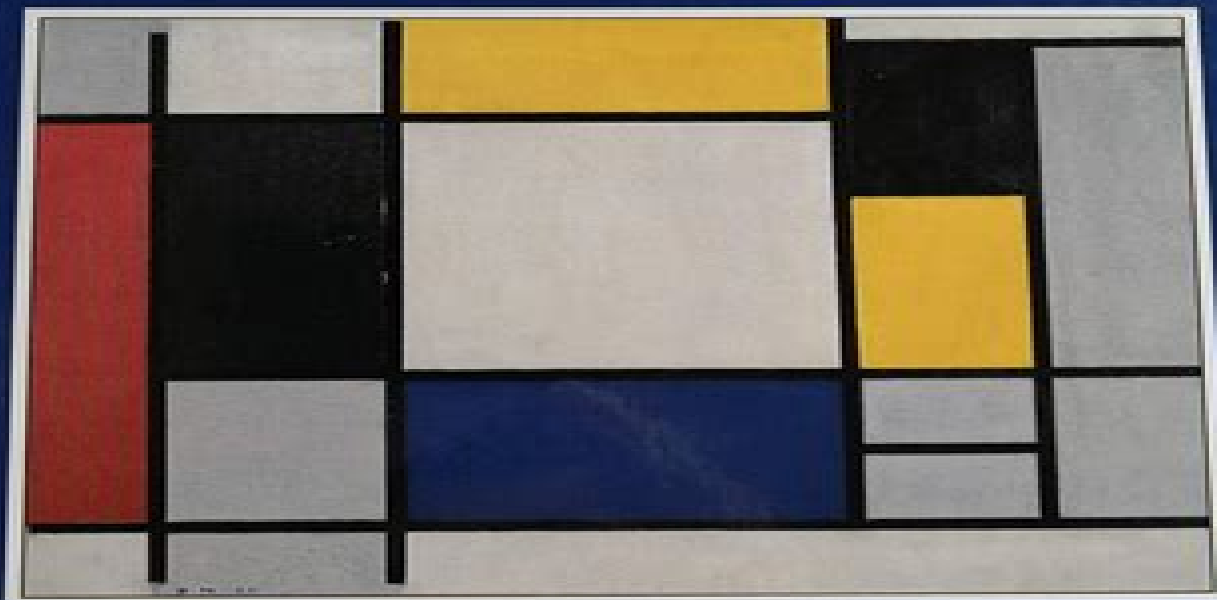


DIGITAL INTEGRATED CIRCUITS

A DESIGN PERSPECTIVE



JAN M. RABAEY

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Digital Integrated Circuits A Design Perspective

Jan M. Rabaey



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Digital Integrated Circuits Jan M. Rabaey, Anantha P. Chandrakasan, Borivoje Nikolić, 2003 Intended for use in undergraduate senior level digital circuit design courses with advanced material sufficient for graduate level courses Progressive in content and form this text successfully bridges the gap between the circuit perspective and system perspective of digital integrated circuit design Beginning with solid discussions on the operation of electronic devices and in depth analysis of the nucleus of digital design the text maintains a consistent logical flow of subject matter throughout The revision addresses today's most significant and compelling industry topics including the impact of interconnect design for low power issues in timing and clocking design methodologies and the tremendous effect of design automation on the digital design perspective The revision reflects the ongoing evolution in digital integrated circuit design especially with respect to the impact of moving into the deep submicron realm

Digital Integrated Circuits Jan M. Rabaey, 1996 Contemporary in content and form this practical book successfully bridges the gap between the circuit perspective and system perspective of digital integrated circuit design Beginning with a solid foundation of the operation of electronic devices and an in depth analysis of the nucleus of digital design it maintains a consistent logical flow of subject matter throughout addressing today's most significant and compelling industry topics the effect of interconnect design for low power issues in timing and clocking design methodologies and the tremendous impact of design automation on the digital design perspective Discusses state of the art topics in design such as complex gates adders multipliers registers controllers and memories Focuses on practical design issues with examples design problems and case studies creating practical knowledge readers can readily apply in industrial design Offers perspectives on the future evolution of design practice at the end of each chapter to put issues discussed in a broader vision Includes many illustrations and reality based design problems to foster comprehension For professional engineers

Digital Integrated Circuits Evgeni Perelroyzen, 2018-10-03 A current trend in digital design the integration of the MATLAB components Simulink and Stateflow for model building simulations system testing and fault detection allows for better control over the design flow process and ultimately for better system results Digital Integrated Circuits Design for Test Using Simulink and Stateflow illustrates the construction of Simulink models for digital project test benches in certain design for test fields The first two chapters of the book describe the major tools used for design for test The author explains the process of Simulink model building presents the main library blocks of Simulink and examines the development of finite state machine modeling using Stateflow diagrams Subsequent chapters provide examples of Simulink modeling and simulation for the latest design for test fields including combinational and sequential circuits controllability and observability deterministic algorithms digital circuit dynamics timing verification built in self test BIST architecture scan cell operations and functional and diagnostic testing The book also discusses the automatic test pattern generation ATPG process the logical determinant theory and joint test action group JTAG interface models Digital Integrated Circuits explores

the possibilities of MATLAB's tools in the development of application specific integrated circuit ASIC design systems The book shows how to incorporate Simulink and Stateflow into the process of modern digital design *Digital Integrated Circuits : Design Perspective(2nd Edition)(Paperback)* Jan M. Rabaey, 2012-01-01 Introduction to VLSI Systems Ming-Bo Lin, 2011-11-28 With the advance of semiconductors and ubiquitous computing the use of system on a chip SoC has become an essential technique to reduce product cost With this progress and continuous reduction of feature sizes and the development of very large scale integration VLSI circuits addressing the harder problems requires fundamental understanding *Outlines and Highlights for Digital Integrated Circuits* Cram101 Textbook Reviews, 2009-12 Never HIGHLIGHT a Book Again Virtually all of the testable terms concepts persons places and events from the textbook are included Cram101 Just the FACTS101 studyguides give all of the outlines highlights notes and quizzes for your textbook with optional online comprehensive practice tests Only Cram101 is Textbook Specific Accompanys 9780130909961 **AN INTRODUCTION TO VLSI DESIGN AND TESTING** P. Brundavani ,B. Abdul Raheem ,Rachapudi Prabahakar , 2025-05-20 VLSI Design and Testing provides a concise yet comprehensive guide to the design analysis and testing of integrated circuits Covering key topics such as IC types Moore's Law MOSFET and CMOS fabrication and SOI technology the book builds a strong foundation in VLSI principles It explores the design flow CMOS logic gates layout techniques and both static and dynamic logic circuits Readers will also learn about circuit performance parameters scaling theory and subsystem design including adders shifters and comparators The book concludes with essential concepts in VLSI design styles FPGA gate array full custom and CMOS testing including fault models ATPG and BIST Ideal for students and professionals it blends theory with practical design strategies in modern VLSI systems Visit garuda publishers com **Advanced VLSI Design and Testability Issues** Suman Lata Tripathi, Sobhit Saxena, Sushanta Kumar Mohapatra, 2020-08-18 This book facilitates the VLSI interested individuals with not only in depth knowledge but also the broad aspects of it by explaining its applications in different fields including image processing and biomedical The deep understanding of basic concepts gives you the power to develop a new application aspect which is very well taken care of in this book by using simple language in explaining the concepts In the VLSI world the importance of hardware description languages cannot be ignored as the designing of such dense and complex circuits is not possible without them Both Verilog and VHDL languages are used here for designing The current needs of high performance integrated circuits ICs including low power devices and new emerging materials which can play a very important role in achieving new functionalities are the most interesting part of the book The testing of VLSI circuits becomes more crucial than the designing of the circuits in this nanometer technology era The role of fault simulation algorithms is very well explained and its implementation using Verilog is the key aspect of this book This book is well organized into 20 chapters Chapter 1 emphasizes on uses of FPGA on various image processing and biomedical applications Then the descriptions enlighten the basic understanding of digital design from the perspective of HDL in Chapters 2 5 The

performance enhancement with alternate material or geometry for silicon based FET designs is focused in Chapters 6 and 7 Chapters 8 and 9 describe the study of bimolecular interactions with biosensing FETs Chapters 10 13 deal with advanced FET structures available in various shapes materials such as nanowire HFET and their comparison in terms of device performance metrics calculation Chapters 14 18 describe different application specific VLSI design techniques and challenges for analog and digital circuit designs Chapter 19 explains the VLSI testability issues with the description of simulation and its categorization into logic and fault simulation for test pattern generation using Verilog HDL Chapter 20 deals with a secured VLSI design with hardware obfuscation by hiding the IC s structure and function which makes it much more difficult to reverse engineer

Current Sense Amplifiers for Embedded SRAM in High-Performance System-on-a-Chip Designs Bernhard Wicht,2013-04-17 System on a chip SoC designs result in a wide range of high complexity high value semiconductor products As the technology scales towards smaller feature sizes and chips grow larger a speed limitation arises due to an increased RC delay associated with interconnection wires Innovative circuit techniques are required to achieve the speed needed for high performance signal processing Current sensing is considered as a promising circuit class since it is inherently faster than conventional voltage sense amplifiers However especially in SRAM current sensing has rarely been used so far Practical implementations are challenging because they require sophisticated analog circuit techniques in a digital environment The objective of this book is to provide a systematic and comprehensive insight into current sensing techniques Both theoretical and practical aspects are covered Design guidelines are derived by systematic analysis of different circuit principles Innovative concepts like compensation of the bit line multiplexer and auto power down will be explained based on theory and experimental results The material will be interesting for design engineers in industry as well as researchers who want to learn about and apply current sensing techniques The focus is on embedded SRAM but the material presented can be adapted to single chip SRAM and to any other current providing memory type as well This includes emerging memory technologies like magnetic RAM MRAM and Ovonic Unified Memory OUM Moreover it is also applicable to array like structures such as CMOS camera chips and to circuits for signal transmission along highly capacitive busses

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