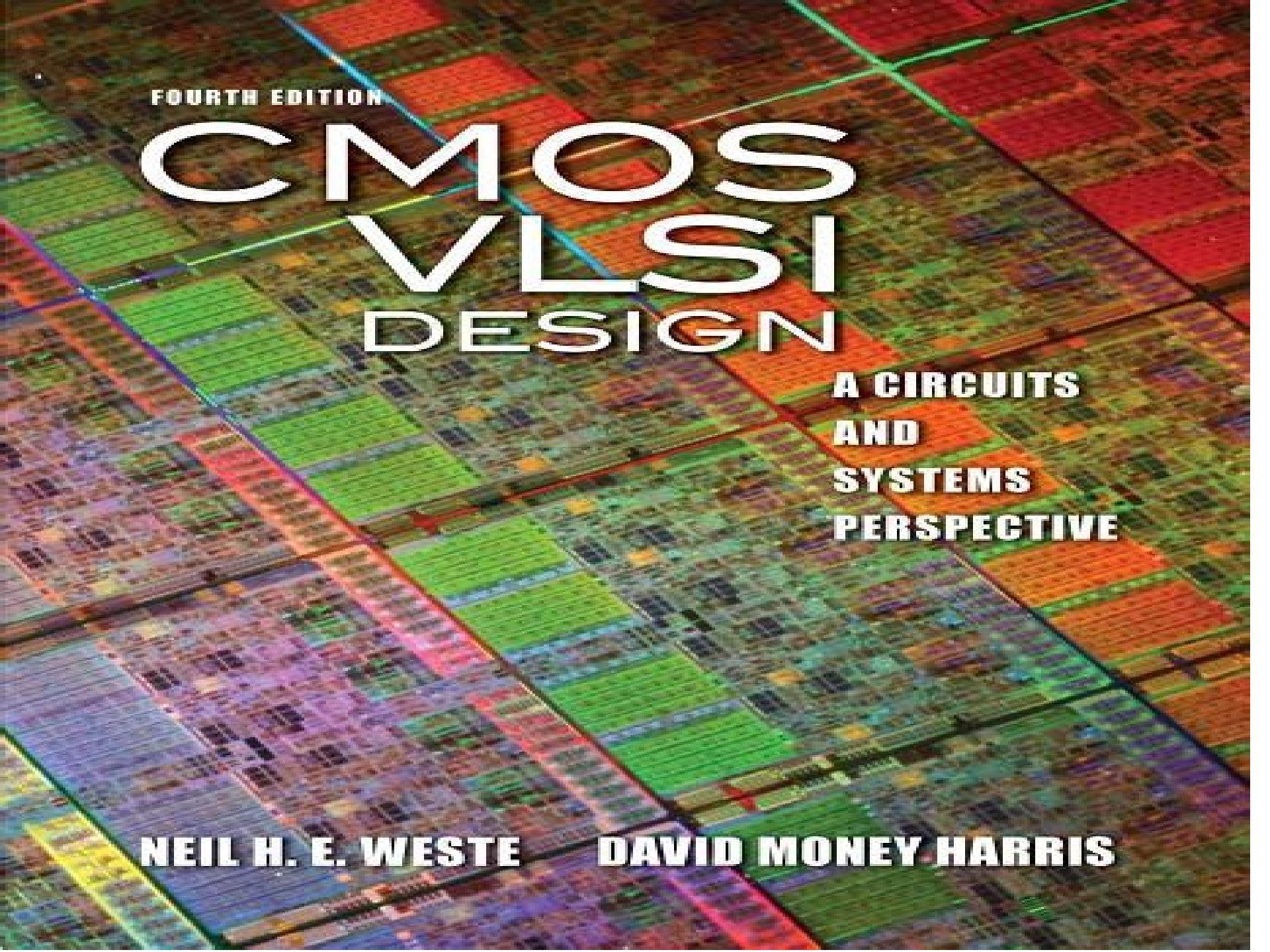




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Transforming Tomorrow: Innovative Solutions and Global Trends in Electrical and Electronics Engineering

Namit Gupta, Anjali Gupta, Naresh Purohit, Shiraz Husain, 2025-06-03 The International Conference on Transforming Tomorrow Innovative Solutions and Global Trends in Electrical and Electronics Engineering Pragyata 2025 is scheduled to be held on May 5 6 2025 at Shri Vaishnav Vidyapeeth Vishwavidyalaya Indore Madhya Pradesh India This prestigious event aims to provide a dynamic platform for researchers academicians industry professionals and students to exchange knowledge showcase cutting edge innovations and discuss global trends shaping the future of Electrical and Electronics Engineering Pragyata 2025 will feature sessions and presentations on key emerging areas including Robotics Renewable Energy Smart Grids Mechatronics 5G Communications Artificial Intelligence and the Internet of Things IoT The conference is designed to foster meaningful dialogue cross disciplinary collaboration and engagement with leading experts from academia and industry In line with its theme of Transforming Tomorrow the conference emphasizes clarity innovation and sustainable development It will serve as a catalyst for forward looking discussions and solutions that address modern engineering challenges and contribute to building a smarter greener and more connected world With a commitment to being Concise Clear and Cohesive Pragyata 2025 is set to become a significant academic and professional milestone in advancing technological progress and inspiring future innovation across the Electrical and Electronics Engineering spectrum [Circuit Design for CMOS VLSI](#)

John P. Uyemura, 2012-12-06 During the last decade CMOS has become increasingly attractive as a basic integrated circuit technology due to its low power at moderate frequencies good scalability and rail to rail operation There are now a variety of CMOS circuit styles some based on static complementary con ductance properties but others borrowing from earlier NMOS techniques and the advantages of using clocking disciplines for precharge evaluate se quencing In this comprehensive book the reader is led systematically through the entire range of CMOS circuit design Starting with the in dividual MOSFET basic circuit building blocks are described leading to a broad view of both combinatorial and sequential circuits Once these circuits are considered in the light of CMOS process technologies impor tant topics in circuit performance are considered including characteristics of interconnect gate delay device sizing and I O buffering Basic circuits are then composed to form macro elements such as multipliers where the reader acquires a unified view of architectural performance through par allelism and circuit performance through careful attention to circuit level and layout design optimization Topics in analog circuit design reflect the growing tendency for both analog and digital circuit forms to be combined on the same chip and a careful treatment of BiCMOS forms introduces the reader to the combination of both FET and bipolar technologies on the same chip to provide improved performance *Simulation and Optimization of Digital Circuits* Vazgen Melikyan, 2018-04-12 This book describes new fuzzy logic based mathematical apparatus which enable readers to work with continuous variables while implementing whole circuit simulations with speed similar to gate level simulators and accuracy similar to circuit level

simulators The author demonstrates newly developed principles of digital integrated circuit simulation and optimization that take into consideration various external and internal destabilizing factors influencing the operation of digital ICs The discussion includes factors including radiation ambient temperature electromagnetic fields and climatic conditions as well as non ideality of interconnects and power rails

Subject Guide to Books in Print ,1996 **Communicating Sequential Processes. The First 25 Years** Ali E. Abdallah,Cliff B. Jones,Jeff W. Sanders,2005-05-04 This volume like the symposium CSP25 which gave rise to it commemorates the semi jubilee of Communicating Sequential Processes 1 Tony Hoare s paper Communicating Sequential Processes is today widely regarded as one of the most influential papers in computer science To commemorate it an event was organized under the auspices of BCS FACS the British Computer Society s Formal Aspects of Computing Science specialist group CSP25 was one of a series of such events organized to highlight the use of formal methods emphasize their relevance to modern computing and promote their wider application BCS FACS is proud that Tony Hoare presented his original ideas on CSP at one of its first meetings in 1978 The two day event 7-8 July 2004 was hosted by London South Bank University s Institute for Computing Research Faculty of Business Computing and Information Management The intention was to celebrate reflect upon and look beyond the first quarter century of CSP s contribution to computer science The meeting examined the impact of CSP on many areas stretching from semantics mathematical models for understanding concurrency and communications and logic for reasoning about behavior through the design of parallel programming languages i o parallelism synchronization and threads to applications varying from distributed software and parallel computing to information security Web services and concurrent hardware circuits It included a panel discussion with panelists Brookes Hoare de Roever and Roscoe chaired by Jeff Sanders poster presentations by PhD students and others featured a fire alarm requiring evacuation in the rain and concluded with the presentation of a fountain pen to Prof Sir C A R Hoare

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Books in Print Supplement ,1994 **FinFET Devices for VLSI Circuits and Systems** Samar K. Saha,2020-07-15 To surmount the continuous scaling challenges of MOSFET devices FinFETs have emerged as the real alternative for use as the next generation device for IC fabrication technology The objective of this book is to provide the basic theory and operating principles of FinFET devices and technology an overview of FinFET device architecture and manufacturing processes and detailed formulation of FinFET electrostatic and dynamic device characteristics for IC design and manufacturing Thus this book caters to practicing engineers transitioning to FinFET technology and prepares the next generation of device engineers and academic experts on mainstream device technology at the nanometer nodes

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