

Using FFT in Cadence Spectre

First, you need to determine your input frequency based on the sampling rate and the number of samples to ensure **coherent sampling**. For example, the sampling rate is $f_s=100\text{MHz}$ and the number of samples (or number of FFT bins) is $N_{\text{FFT}}=2^6=64$. If we want the input frequency (f_{in}) is around 10MHz , the input frequency required for coherent sampling is given by:

- 1) Calculate $\frac{f_{\text{desire}}}{f_s} \times N_{\text{FFT}} = \frac{10}{100} \times 2^6 = 6.4$
- 2) Find the closest prime number to $\left\lfloor \frac{f_{\text{desire}}}{f_s} \times N_{\text{FFT}} \right\rfloor$, that is 7.
- 3) Then the actual input frequency is $\frac{7}{N_{\text{FFT}}} \times f_s = 10.9375\text{MHz}$

and the input bin is $f_{\text{bin}} = 7$.

In this example, the output of a residue amplifier needs to be analyzed. The RA generates $2X$ gain for a 1.5 bit pipeline stage.



First we set the input frequency to be $7/64 \times 100\text{MHz}$.

Cadence Spectre Manual

**Matthew Laudon, Bartłomiej F.
Romanowicz**



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MOSFET Modeling & BSIM3 User's Guide Yuhua Cheng, Chenming Hu, 2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike [Sigma-Delta Converters: Practical Design Guide](#) Jose M. de la Rosa, 2018-08-22 Thoroughly revised and expanded to help readers systematically increase their knowledge and insight about Sigma Delta Modulators Sigma Delta Modulators SDMs have become one of the best choices for the implementation of analog digital interfaces of electronic systems integrated in CMOS technologies Compared to other kinds of Analog to Digital Converters ADCs Ms cover one of the widest conversion regions of the resolution versus bandwidth plane being the most efficient solution to digitize signals in an increasingly number of applications which span from high resolution low bandwidth digital audio sensor interfaces and instrumentation to ultra low power biomedical systems and medium resolution broadband wireless communications Following the spirit of its first edition Sigma Delta Converters Practical Design Guide 2nd Edition takes a comprehensive look at SDMs their diverse types of architectures circuit techniques analysis synthesis methods and CAD tools as well as their practical design considerations It compiles and updates the current research reported on the topic and explains the multiple trade offs involved in the whole design flow of Sigma Delta Modulators from specifications to chip implementation and characterization The book follows a top down approach in order to provide readers with the necessary understanding about recent advances trends and challenges in state of the art

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Electronics John D. Cressler,H. Alan Mantooth,2017-12-19 Unfriendly to conventional electronic devices circuits and systems extreme environments represent a serious challenge to designers and mission architects The first truly comprehensive guide to this specialized field Extreme Environment Electronics explains the essential aspects of designing and using devices circuits and electronic systems intended to operate in extreme environments including across wide temperature ranges and in radiation intense scenarios such as space The Definitive Guide to Extreme Environment Electronics Featuring contributions by some of the world s foremost experts in extreme environment electronics the book provides in depth information on a wide array of topics It begins by describing the extreme conditions and then delves into a description of suitable semiconductor technologies and the modeling of devices within those technologies It also discusses reliability issues and failure mechanisms that readers need to be aware of as well as best practices for the design of these electronics Continuing beyond just the paper design of building blocks the book rounds out coverage of the design realization process with verification techniques and chapters on electronic packaging for extreme environments The final set of chapters describes actual chip level designs for applications in energy and space exploration Requiring only a basic background in electronics the book combines theoretical and practical aspects in each self contained chapter Appendices supply additional background material With its broad coverage and depth and the expertise of the contributing authors this is an invaluable reference for engineers scientists and technical managers as well as researchers and graduate students A hands on resource it explores what is required to successfully operate electronics in the most demanding conditions ESD Design and

Analysis Handbook James E. Vinson,Joseph C. Bernier,Gregg D. Croft,Juin Jei Liou,2012-12-06 ESD Design and Analysis Handbook presents an overview of ESD as it effects electronic circuits and provides a concise introduction for students engineers circuit designers and failure analysts This handbook is written in simple terms and is filled with practical advice and examples to illustrate the concepts presented While this treatment is not exhaustive it presents many of the most important areas of the ESD problem and suggests methods for improving them The key topics covered include the physics of the event failure analysis protection characterization and simulation techniques The book is intended as both an introductory

text on ESD and a useful reference tool to draw on as the reader gains experience The authors have tried to balance the level of detail in the ESD Design and Analysis Handbook against the wealth of literature published on ESD every year To that end each chapter has a topical list of references to facilitate further in depth study

CMOS Sigma-Delta Converters Jose M. de la Rosa, Rocio del Rio, 2013-03-13 A comprehensive overview of Sigma Delta Analog to Digital Converters ADCs and a practical guide to their design in nano scale CMOS for optimal performance This book presents a systematic and comprehensive compilation of sigma delta converter operating principles the new advances in architectures and circuits design methodologies and practical considerations going from system level specifications to silicon integration packaging and measurements with emphasis on nanometer CMOS implementation The book emphasizes practical design issues from high level behavioural modelling in MATLAB SIMULINK to circuit level implementation in Cadence Design FrameWork II As well as being a comprehensive reference to the theory the book is also unique in that it gives special importance on practical issues giving a detailed description of the different steps that constitute the whole design flow of sigma delta ADCs The book begins with an introductory survey of sigma delta modulators their fundamentals architectures and synthesis methods covered in Chapter 1 In Chapter 2 the effect of main circuit error mechanisms is analysed providing the necessary understanding of the main practical issues affecting the performance of sigma delta modulators The knowledge derived from the first two chapters is presented in the book as an essential part of the systematic top down bottom up synthesis methodology of sigma delta modulators described in Chapter 3 where a time domain behavioural simulator named SIMSIDES is described and applied to the high level design and verification of sigma delta ADCs Chapter 4 moves farther down from system level to the circuit and physical level providing a number of design recommendations and practical recipes to complete the design flow of sigma delta modulators To conclude the book Chapter 5 gives an overview of the state of the art sigma delta ADCs which are exhaustively analysed in order to extract practical design guidelines and to identify the incoming trends design challenges as well as practical solutions proposed by cutting edge designs Offers a complete survey of sigma delta modulator architectures from fundamentals to state of the art topologies considering both switched capacitor and continuous time circuit implementations Gives a systematic analysis and practical design guide of sigma delta modulators from a top down bottom up perspective including mathematical models and analytical procedures behavioural modeling in MATLAB SIMULINK macromodeling and circuit level implementation in Cadence Design FrameWork II chip prototyping and experimental characterization Systematic compilation of cutting edge sigma delta modulators Complete description of SIMSIDES a time domain behavioural simulator implemented in MATLAB SIMULINK Plenty of examples case studies and simulation test benches covering the different stages of the design flow of sigma delta modulators A number of electronic resources including SIMSIDES the statistical data used in the state of the art survey as well as many design examples and test benches are hosted on a companion website Essential reading for Researchers and electronics engineering practitioners

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High Performance CMOS Range Imaging Andreas Süss, 2016-03-24 This work is dedicated to CMOS based imaging with the emphasis on the noise modeling characterization and optimization in order to contribute to the design of high performance imagers in general and range imagers in particular CMOS is known to be superior to CCD due to its flexibility in terms of integration capabilities but typically has to be enhanced to compete at parameters as for instance noise dynamic range or spectral response This work gathers the widespread theory on noise and extends the theory by a non rigorous but potentially computing efficient algorithm to estimate noise in time sampled systems

Noise in Devices and Circuits, 2003

A Computer-Aided Design and Synthesis Environment for Analog Integrated Circuits Geert Van der Plas, Georges Gielen, Willy M.C. Sansen, 2002-04-30 This text addresses the design methodologies and CAD tools available for the systematic design and design automation of analogue integrated circuits Two complementary approaches discussed increase analogue design productivity demonstrated throughout using design times of the different design experiments undertaken

Analog Circuits and Systems Optimization based on Evolutionary Computation Techniques Manuel Barros, Jorge Guilherme, Nuno Horta, 2010-04-22 The microelectronics market with special emphasis to the production of complex mixed signal systems on chip SoC is driven by three main dynamics time market productivity and managing complexity Pushed by the progress in nanometer technology the design teams are facing a curve of complexity that grows exponentially thereby slowing down the productivity design rate Analog design automation tools are not developing at the same pace of technology once custom design characterized by decisions taken at each step of the analog design flow lies most of the time on designer knowledge and expertise Actually the use of sign management platforms like the Cadences Virtuoso platform with a set of tegrated CAD tools and database facilities to deal with the design transformations from the system level to the physical implementation can significantly speed up the design process and enhance the productivity of analog mixed signal integrated circuit IC design teams These design management platforms are a valuable help in analog IC design but they are still far behind the development stage of design automation tools already available for digital design Therefore the development of new CAD tools and design methodologies for analog and mixed signal ICs is ess tial to increase the designer s productivity and reduce design productivitygap The work presented in this book describes a new design automation approach to the problem of sizing analog ICs

2001 International Conference on Modeling and Simulation of Microsystems Matthew Laudon, Bartlomiej F. Romanowicz, 2001 The worlds most comprehensive and up to date collection of Multidisciplinary Micro and Nano technical papers Technical Proceedings of the 2001 International Conference on Modeling and Simulation of Microsystems Micro and Nano Fluidic Systems MEMS System Optimization MEMS Applications and Characterization Advanced Numerics Process Modeling Quantum Effects Quantum Devices Spintronics Atomistic of Silicon Processing Advanced Semiconductors Circuit Modeling Compact Modeling Papers taken from the 2001 MSM Hilton Head

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