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About the Author

James A. Hall is a Professor of Accounting, co-director of the Computer Science and Business program, and the Peter E. Bennett Chair in Business and Economics at Lehigh University in Bethlehem, PA. After his discharge from the U.S. Army, he entered the University of Tulsa in 1970 and received a B.S.B.A. in 1974 and an M.B.A. in 1976. He earned his Ph.D. from Oklahoma State University in 1979. Dr. Hall has worked in the field of systems analysis and computer auditing and has served as consultant in these areas to numerous organizations. Dr. Hall has published articles in the Journal of Accounting, Auditing & Finance, Journal of MIS, Communications of the ACM, Journal Of Management Systems, Management Accounting, Journal Of Computer Information Systems, The Journal Of Accounting Education, The Review Of Accounting Information Systems, and other professional journals. He is also the author of ACCOUNTING INFORMATION SYSTEMS and INFORMATION TECHNOLOGY AUDITING, both published by

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Reliable up to date and versatile Control System Fundamentals answers your basic control systems questions and acts as an ideal starting point for approaching any control problem Volume 1 of Inter. Acct, 10th Edition with Update Chapter Donald E. Kieso, Jerry J. Weygandt, Terry D. Warfield, 2002-02-11 **Synchronization and Arbitration in Digital Systems** David J. Kinniment, 2008-02-28 Today's networks of processors on and off chip operating with independent clocks need effective synchronization of the data passing between them for reliability When two or more processors request access to a common resource such as a memory an arbiter has to decide which request to deal with first Current developments in integrated circuit processing are leading to an increase in the numbers of independent digital processing elements in a single system With this comes faster communications more networks on chip and the demand for more reliable more complex and higher performance synchronizers and arbiters Written by one of the foremost researchers in this area of digital design this authoritative text provides in depth theory and practical design solutions for the reliable working of synchronization and arbitration hardware in digital systems The book provides methods for making real reliability measurements both on and off chip evaluating some of the common difficulties and detailing circuit solutions at both circuit and system levels Synchronization and Arbitration in Digital Systems also presents mathematical models used to estimate mean time between failures in digital systems a summary of serial and parallel communication techniques for on chip data transmission explanations on how to design a wrapper for a locally synchronous cell highlighting the issues associated with stoppable clocks an examination of various types of priority arbiters using signal transition graphs to show the specification of different designs from the simplest to more complex multi way arbiters including ways of solving problems encountered in a wide range of applications essential information on systems composed of independently timed regions including a discussion on the problem of choice and the factors affecting the time taken to make choices in electronics With its logical approach to design methodology this will prove an invaluable guide for electronic and computer engineers and researchers working on the design of digital electronic hardware Postgraduates and senior undergraduate students studying digital systems design as part of their electronic engineering course will struggle to find a resource that better details the information given inside this book

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