

A CADENCE ALLEGRO

PCB-TERVEZÉS CAD-SZOFTVER

(15.3)

FEJLŐPÉS:

- I. ALKALMAZÁS
- II. PCB TERVEZÉS
- III. HIBÁ-ÉRTÉKELÉS
- IV. SZIMULÁCIÓK (Simulate)

I. ALKALMAZÁS

KEZDŐKÉRDÉSEK



- A FILE MENÜ:
 - OPEN: Megnyitja a kiválasztott fájlt.
 - SAVE: Mentés.
 - PRINT: Nyomtatás.

- MENU: A menürendszer.

CONTROL PANEL

- A CONTROL PANEL a bal oldali panel, amely a különböző funkciókhoz tartozó gombokat tartalmazza.
- A gombokat a bal oldali panelen lehet elhelyezni, és a jobb oldali panelen lehet kiválasztani.
- A gombokat a bal oldali panelen lehet elhelyezni, és a jobb oldali panelen lehet kiválasztani.

ADDED MESSAGES

- A MESSAGE MENÜ a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.
- A MESSAGE MENÜ a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

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- A MESSAGE MENÜ a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

RETEGEK:

Allegroban a rétegek a következők: TOP, BOTTOM, PADS, DRILL, etc.

PORTABLES CLASS

- TOP: A top réteg.
- BOTTOM: A bottom réteg.
- PADS: A pad réteg.
- DRILL: A drill réteg.

EXTRA CLASS

- TOP: A top réteg.
- BOTTOM: A bottom réteg.
- PADS: A pad réteg.
- DRILL: A drill réteg.

ADDITIONAL REVERSE

- A REVERSE a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

TEXT & SYMBOLS

- A TEXT & SYMBOLS a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

SYMBOLS

- A SYMBOLS a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.
- A SYMBOLS a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

PCB MANIPULATION

- A PCB MANIPULATION a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.
- A PCB MANIPULATION a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

- KEEPIN: A KEEPIN a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.
- A KEEPIN a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

SOLDER MASK

- A SOLDER MASK a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

SZÍNEK / COLORES

A SZÍNEK a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

DISPLAY & COLORED

- A DISPLAY & COLORED a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

SPIN

- A SPIN a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.
- A SPIN a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

SPIN DISPLAY

- A SPIN DISPLAY a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.
- A SPIN DISPLAY a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

DISPLAY & COLORED

- A DISPLAY & COLORED a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.
- A DISPLAY & COLORED a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

MESSAGE

- A MESSAGE a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.
- A MESSAGE a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

ZOOM

EGGER (with mouse)

- A ZOOM a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.
- A ZOOM a bal oldali panelen található, és a jobb oldali panelen lehet kiválasztani.

Cadence Allegro User Manual

Jack W. Plunkett



Cadence Allegro User Manual:

Complete PCB Design Using OrCAD Capture and PCB Editor Kraig Mitzner, Bob Doe, Alexander Akulin, Anton Suponin, Dirk Müller, 2019-06-20 Complete PCB Design Using OrCAD Capture and PCB Editor Second Edition provides practical instruction on how to use the OrCAD design suite to design and manufacture printed circuit boards Chapters cover how to Design a PCB using OrCAD Capture and OrCAD PCB Editor adding PSpice simulation capabilities to a design how to develop custom schematic parts how to create footprints and PSpice models and how to perform documentation simulation and board fabrication from the same schematic design This book is suitable for both beginners and experienced designers providing basic principles and the program s full capabilities for optimizing designs Companion site <https://www.elsevier.com/books-and-journals/book-companion/9780128176849> Presents a fully updated edition on OrCAD Capture Version 17.2 Combines the theoretical and practical parts of PCB design Includes real life design examples that show how and why designs work providing a comprehensive toolset for understanding OrCAD software Provides the exact order in which a circuit and PCB are designed Introduces the IPC JEDEC and IEEE standards relating to PCB design [Sigma-Delta Converters: Practical Design Guide](#) Jose M. de la Rosa, 2018-08-22 Thoroughly revised and expanded to help readers systematically increase their knowledge and insight about Sigma Delta Modulators Sigma Delta Modulators SDMs have become one of the best choices for the implementation of analog digital interfaces of electronic systems integrated in CMOS technologies Compared to other kinds of Analog to Digital Converters ADCs SDMs cover one of the widest conversion regions of the resolution versus bandwidth plane being the most efficient solution to digitize signals in an increasingly number of applications which span from high resolution low bandwidth digital audio sensor interfaces and instrumentation to ultra low power biomedical systems and medium resolution broadband wireless communications Following the spirit of its first edition Sigma Delta Converters Practical Design Guide 2nd Edition takes a comprehensive look at SDMs their diverse types of architectures circuit techniques analysis synthesis methods and CAD tools as well as their practical design considerations It compiles and updates the current research reported on the topic and explains the multiple trade offs involved in the whole design flow of Sigma Delta Modulators from specifications to chip implementation and characterization The book follows a top down approach in order to provide readers with the necessary understanding about recent advances trends and challenges in state of the art SDMs It makes more emphasis on two key points which were not treated so deeply in the first edition It includes a more detailed explanation of SDMs implemented using Continuous Time CT circuits going from system level synthesis to practical circuit limitations It provides more practical case studies and applications as well as a deeper description of the synthesis methodologies and CAD tools employed in the design of converters Sigma Delta Converters Practical Design Guide 2nd Edition serves as an excellent textbook for undergraduate and graduate students in electrical engineering as well as design engineers working on SD data converters who are looking for a uniform and self contained

reference in this hot topic With this goal in mind and based on the feedback received from readers the contents have been revised and structured to make this new edition a unique monograph written in a didactical pedagogical and intuitive style

Plunkett's Engineering & Research Industry Almanac 2006: The Only Complete Guide to the Business of Research, Development and Engineering Jack W. Plunkett, 2006-05 A complete guide to trends and leading companies in the Engineering and Research business fields design development and technology based research Includes market analysis R D data and several statistical tables Nearly 400 in depth profiles of Engineering and Research firms

Analog Design and Simulation Using OrCAD Capture and PSpice Dennis Fitzpatrick, 2017-12-11 New to this edition Updated to using OrCAD Release 17.2 and its new features Coverage of PSpice extra features PSpice Designer PSpice Designer Plus Modelling Application PSpice Part Search Symbol Viewer PSpice Report Associate PSpice model New delay functions for Behavioural Simulation Models New Models Support for negative values in hysteresis voltage and threshold voltage A new chapter on PSpice Advanced Analysis Analog Design and Simulation Using OrCAD Capture and PSpice Second Edition provides step by step instructions on how to use the Cadence OrCAD family of Electronic Design Automation software for analog design and simulation The book explains how to enter schematics in Capture set up project types project libraries and prepare circuits for PSpice simulation There are chapters on the different analysis types for DC Bias point DC sweep AC frequency sweep Parametric analysis Temperature analysis Performance Analysis Noise analysis Sensitivity and Monte Carlo simulation Subsequent chapters explain how the Stimulus Editor is used to define custom analog and digital signals how the Model Editor is used to view and create new PSpice models and Capture parts and how the Magnetic Parts Editor is used to design transformers and inductors Other chapters include Analog Behavioral models Test Benches as well as how to create hierarchical designs The book includes the latest features in the OrCAD 17.2 release and there are exercises with step by step instructions at the end of each chapter that enables the reader to progress based upon their experience and knowledge gained from previous chapters The author worked for Cadence for over eight years and supported and delivered OrCAD PSpice training courses all over Europe This book has been endorsed by Cadence In addition there are new chapters on the PSpice Advanced Analysis suite of tools Sensitivity Analysis Optimizer Monte Carlo and Smoke Analysis The chapters show how circuit performance can effectively be maximised and optimised for variations in component tolerances temperature effects manufacturing yields and component stress Provides both a comprehensive user guide and a detailed overview of simulation using OrCAD Capture and PSpice Includes worked and ready to try sample designs and a wide range of to do exercises Covers Capture and PSpice together

Bogatin's Practical Guide to Prototype Breadboard and PCB Design Eric Bogatin, 2021-09-30 Printed circuit boards PCB are at the heart of every electronic product manufactured today Yet engineers rarely learn to design PCBs from a class or course They learn it by doing by reading app notes watching YouTube videos and sitting by the side of an experienced engineer This book is the foundation building book for all engineers starting

out to design PCBs It teaches good habits designing a PCB first for connectivity and secondly introduces the four most important principles to reduce noise A seven step process is presented developing a plan of record creating a Bill of Materials completing the schematic completing the layout completing the assembly conducting bring up and troubleshooting and documenting the project Each step is developed in detail In particular the emphasis in this book is on risk management what can be done at each step of the process to reduce the risk of a hard error which requires a complete re spin or a soft error which requires some sort of on the fly repair After connectivity is designed it s important to develop good habits to minimize the potential noise from ground bounce power rail stitching noise stack up design and reducing switching noise in signal paths These techniques apply to all designs from 2 layer to 8 layer and more for bandwidths below 200 MHz The best practices for manual lead free soldering are presented so that everyone can become a soldering expert The best measurement practices using common lab instruments such as the DMM the constant current constant voltage power supply and oscilloscopes are presented so that common artifacts are minimized Features in the design that help you find design or assembly errors quickly and the troubleshooting techniques to find and fix problems are introduced Applying the habits presented in this book will help every engineer design their next circuit board faster with less chance of an unexpected problem with the lowest noise This textbook will also have embedded videos to visually demonstrate many of the hands on processes introduced in this book

Noise Coupling in System-on-Chip Thomas Noulis, 2018-01-09 Noise Coupling is the root cause of the majority of Systems on Chip SoC product fails The book discusses a breakthrough substrate coupling analysis flow and modelling toolset addressing the needs of the design community The flow provides capability to analyze noise components propagating through the substrate the parasitic interconnects and the package Using this book the reader can analyze and avoid complex noise coupling that degrades RF and mixed signal design performance while reducing the need for conservative design practices With chapters written by leading international experts in the field novel methodologies are provided to identify noise coupling in silicon It additionally features case studies that can be found in any modern CMOS SoC product for mobile communications automotive applications and readout front ends

Plunkett's Infotech Industry Almanac 2006: The Only Complete Guide to the Technologies and Companies Changing the Way the World Thinks, Works and Shar Jack W. Plunkett, 2006 Plunkett's InfoTech Industry Almanac presents a complete analysis of the technology business including the convergence of hardware software entertainment and telecommunications This market research tool includes our analysis of the major trends affecting the industry from the rebound of the global PC and server market to consumer and enterprise software to super computers open systems such as Linux web services and network equipment In addition we provide major statistical tables covering the industry from computer sector revenues to broadband subscribers to semiconductor industry production No other source provides this books easy to understand comparisons of growth expenditures technologies imports exports corporations research and other vital subjects The

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